

Bid Document

Bid Details	
Bid End Date/Time	30-11-2022 17:00:00
Bid Opening Date/Time	30-11-2022 17:30:00
Bid Offer Validity (From End Date)	90 (Days)
Ministry/State Name	Pmo
Department Name	Department Of Atomic Energy
Organisation Name	Iter India Ipr
Office Name	literindia Ipr Blocka Sangath Skyz Bhatmote
Total Quantity	10
Item Category	DISCRETE SEMICONDUCTOR DEVICES (ACTIVE COMPONENT - TRANSISTOR) (Q3)
MSE Exemption for Years of Experience and Turnover	No
Startup Exemption for Years of Experience and Turnover	No
Document required from seller	OEM Authorization Certificate *In case any bidder is seeking exemption from Experience / Turnover Criteria, the supporting documents to prove his eligibility for exemption must be uploaded for evaluation by the buyer
Bid to RA enabled	No
Time allowed for Technical Clarifications during technical evaluation	5 Days
Evaluation Method	Total value wise evaluation

EMD Detail

Required	No
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ePBG Detail

Required	No
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Splitting

Bid splitting not applied.

MII Purchase Preference

MII Purchase Preference	Yes
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MSE Purchase Preference

MSE Purchase Preference	Yes
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1. Preference to Make In India products (For bids < 200 Crore):Preference shall be given to Class 1 local supplier as defined in public procurement (Preference to Make in India), Order 2017 as amended from time to time and its subsequent Orders/Notifications issued by concerned Nodal Ministry for specific Goods/Products. The minimum local content to qualify as a Class 1 local supplier is denoted in the bid document. If the bidder wants to avail the Purchase preference, the bidder must upload a certificate from the OEM regarding the percentage of the local content and the details of locations at which the local value addition is made along with their bid, failing which no purchase preference shall be granted. In case the bid value is more than Rs 10 Crore, the declaration relating to percentage of local content shall be certified by the statutory auditor or cost auditor, if the OEM is a company and by a practicing cost accountant or a chartered accountant for OEMs other than companies as per the Public Procurement (preference to Make-in -India) order 2017 dated 04.06.2020. Only Class-I and Class-II Local suppliers as per MII order dated 4.6.2020 will be eligible to bid. Non - Local suppliers as per MII order dated 04.06.2020 are not eligible to participate. However, eligible micro and small enterprises will be allowed to participate .In case Buyer has selected Purchase preference to Micro and Small Enterprises clause in the bid, the same will get precedence over this clause.

2. Purchase preference to Micro and Small Enterprises (MSEs): Purchase preference will be given to MSEs as defined in Public Procurement Policy for Micro and Small Enterprises (MSEs) Order, 2012 dated 23.03.2012 issued by Ministry of Micro, Small and Medium Enterprises and its subsequent Orders/Notifications issued by concerned Ministry. If the bidder wants to avail the Purchase preference, the bidder must be the manufacturer of the offered product in case of bid for supply of goods. Traders are excluded from the purview of Public Procurement Policy for Micro and Small Enterprises. In respect of bid for Services, the bidder must be the Service provider of the offered Service. Relevant documentary evidence in this regard shall be uploaded along with the bid in respect of the offered product or service. If L-1 is not an MSE and MSE Seller (s) has/have quoted price within L-1+ 15% (Selected by Buyer)of margin of purchase preference /price band defined in relevant policy, such Seller shall be given opportunity to match L-1 price and contract will be awarded for 100%(selected by Buyer) percentage of total QUANTITY.

DISCRETE SEMICONDUCTOR DEVICES (ACTIVE COMPONENT - TRANSISTOR) (10 packet)

(Minimum 50% and 20% Local Content required for qualifying as Class 1 and Class 2 Local Supplier respectively)

Brand Type	Unbranded
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Technical Specifications

[* As per GeM Category Specification](#)

Specification	Specification Name	Bid Requirement (Allowed Values)
TYPE/APPLICATION	Application	Amplification
	Transistor type	MOSFET
	Type number/Part number	2T203, 2T206, 2N200
	Case style / Package type	TO-3, TO-5, TO-8, TO-18, TO-36, TO-39, TO- 46, TO-52, TO-66, TO-72, TO-92, TO-126, TO-202, TO-218, TO-220, TO-226, TO-254, TO-257, TO-258, TO-259, TO-264, TO-267

Specification	Specification Name	Bid Requirement (Allowed Values)
GENERIC	Construction Type	N Type Channel
	Number of units/transistors in a pack(Per Packet)	10
TEST REPORT	Type of lab which carried out Test of Complete Product to prove the conformity of product as per specification	NABL accredited lab, NA

Consignees/Reporting Officer and Quantity

S.No.	Consignee/Reporting Officer	Address	Quantity	Delivery Days
1	Mayurkumar Navinchandra Patel	380005,ITER-INDIA, IPR, BLOCK-A, SANGATH SKYZ, BHAT-MOTERA ROAD, KOTESHWAR, AHMEDABAD-380005, GUJARAT, INDIA	10	60

Buyer Added Bid Specific Terms and Conditions

1. Generic

OPTION CLAUSE: The Purchaser reserves the right to increase or decrease the quantity to be ordered up to 25 percent of bid quantity at the time of placement of contract. The purchaser also reserves the right to increase the ordered quantity by up to 25% of the contracted quantity during the currency of the contract at the contracted rates. Bidders are bound to accept the orders accordingly.

2. Scope of Supply

Scope of supply (Bid price to include all cost components) : Only supply of Goods

3. Generic

Upload Manufacturer authorization: Wherever Authorised Distributors are submitting the bid, Manufacturers Authorisation Form (MAF)/Certificate with OEM details such as name, designation, address, e-mail Id and Phone No. required to be furnished along with the bid.

4. Buyer Added Bid Specific ATC

Buyer Added text based ATC clauses

4.1. Bidder are requested to fill Bidder's compliance column (duly signed and stamped) of Technical Specifications as per Annexure-A. Bidder needs to sign & stamp on each & every page of attached Annexure-A. This will be considered as an acceptance of bidder on Annexure-A.

4.2. Bidder are requested to note that Technical Evaluation of the bid is based on technical compliance

sheet attached in buyer added bid specific ATC documents & authorization from manufacturer.

4.3 Bidder are requested to note following amendment in technical specification of the GeM Bid and submit their bids in line with Revised Values as mentioned below.

Specification Name	Bid Requirement (Allowed Values)	Revised Bid Requirement (Allowed Values)
Type number/Part number	2T203, 2T206, 2N200	To be defined by bidder
Case style / Package type	TO-3, TO-5, TO-8, TO-18, TO-36, TO-39, TO- 46, TO-52, TO-66, TO-72, TO-92, TO-126, TO-202, TO-218, TO-220, TO-226, TO-254, TO-257, TO-258, TO-259, TO-264, TO-267	NI--1230H--4S
Construction Type	N Type Channel	N-Channel Enhancement Mode Lateral MOSFET
Type of lab which carried out Test of Complete Product to prove the conformity of product as per specification	NABL accredited lab, NA	NA

4.4 Bidder needs to provide items complying to the specification as per attached Annexure-A.

5. Buyer Added Bid Specific ATC

Buyer uploaded ATC document [Click here to view the file.](#)

Disclaimer

The additional terms and conditions have been incorporated by the Buyer after approval of the Competent Authority in Buyer Organization. Buyer organization is solely responsible for the impact of these clauses on the bidding process, its outcome, and consequences thereof including any eccentricity/restriction arising in the bidding process due to these ATCs and due to modification of technical specifications and/or terms and conditions governing the bid. Any clause incorporated by the Buyer such as demanding Tender Sample, incorporating any clause against the MSME policy and Preference to make in India Policy, mandating any Brand names or Foreign Certification, changing the default time period for Acceptance of material or payment timeline governed by OM of Department of Expenditure shall be null and void and would not be considered part of bid. Further any reference of conditions published on any external site or reference to external documents/clauses shall also be null and void. If any seller has any objection/grievance against these additional clauses or otherwise on any aspect of this bid, they can raise their representation against the same by using the Representation window provided in the bid details field in Seller dashboard after logging in as a seller within 4 days of bid publication on GeM. Buyer is duty bound to reply to all such representations and would not be allowed to open bids if he fails to reply to such representations. Also, GeM does not permit collection of Tender fee / Auction fee in case of Bids / Forward Auction as the case may be. Any stipulation by the Buyer seeking payment of Tender Fee / Auction fee through ATC clauses would be treated as null and void.

[This Bid is also governed by the General Terms and Conditions](#)

In terms of GeM GTC clause 26 regarding Restrictions on procurement from a bidder of a country which shares a land border with India, any bidder from a country which shares a land border with India will be eligible to bid in this tender only if the bidder is registered with the Competent Authority. While participating in bid, Bidder has to undertake compliance of this and any false declaration and non-compliance of this would be a ground for immediate termination of the contract and further legal action in accordance with the laws.

---Thank You---



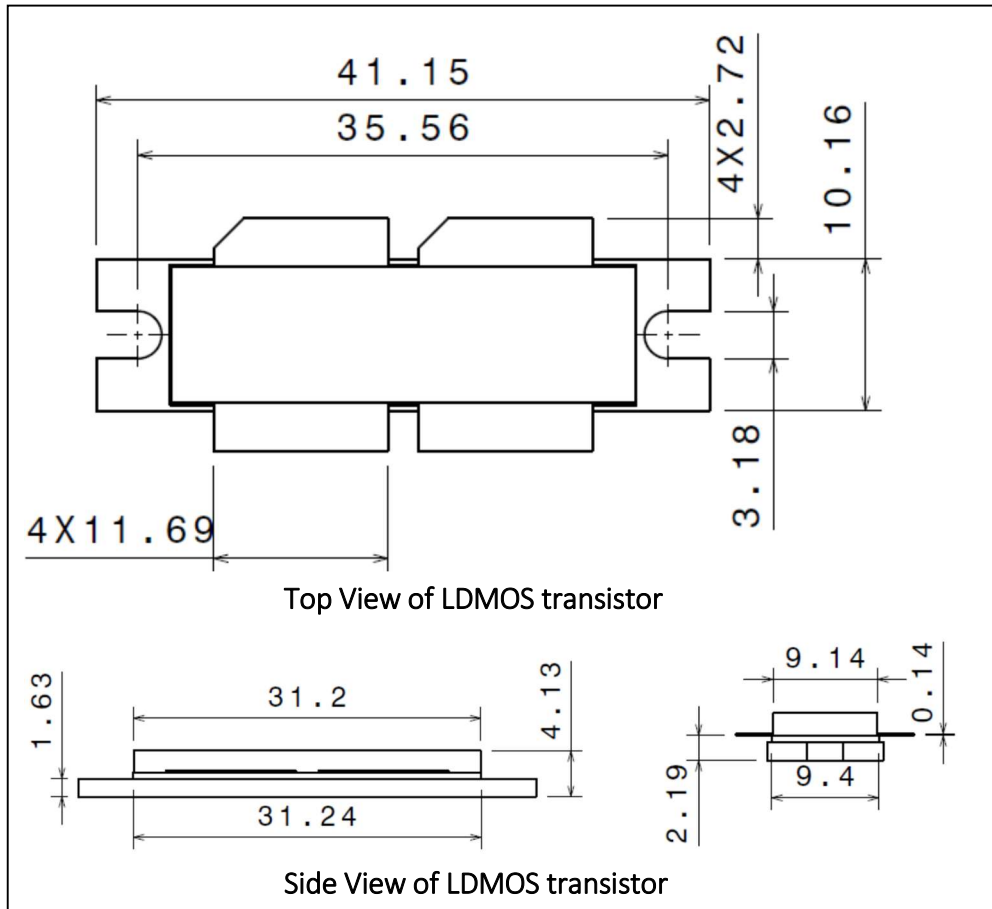
Annexure -A : Technical Compliance Sheet

Table 1: Technical Specification

Sr No.	ITER-INDIA specifications	Bidder Specifications	Comply Y/N	Comment
1.	Transistor type: MOSFET,			
2.	Type number/Part number: To be defined by bidder			
3.	Frequency: 1.8 MHz to 400 MHz			
4.	RF output power: 1800 W (At 27 MHz /CW operation, Vdd 65 V)			
5.	Gain: 27.8 dB (At 27 MHz /CW operation, Vdd 65 V)			
6.	Efficiency: Better than 75% (At 27 MHz /CW operation, Vdd 65V)			
7.	Case style / Package type: NI--1230H--4S			
8.	Application: Amplification			
9.	Construction Type: N-Channel Enhancement Mode Lateral MOSFET			
10.	Packing: N.A.			
11.	Number of units/transistors in a pack (Per Packet): N.A.			
12.	Total Device dissipation (Maximum): 2247 W (TC = 25 deg. C)			
13.	Junction temperature (Maximum): -40 to +225 deg. C			
14.	Thermal Resistance Junction to ambient air (Deg C/W): N.A.			
15.	Thermal Resistance Junction to Case (Deg C/W): 0.09			
16.	URL of the uploaded product catalogue: N.A.			
17.	Features: RF Power LDMOS Transistor			

18.	Type of lab which carried out Test of Complete Product to prove the conformity of product as per specification: N.A.			
19.	Test Report Number and Date: N.A.			
20.	Name and address of the Lab where Test conducted: N.A.			
21.	Quantity: 10 Nos			

Drawing No 1: Case drawing for LDMOS transistor



Bidder Signature		
Name of the signatory & Title	Name	Title
Bidder's Official seal		
Place & Date	Place	DD-MM-YYYY